



Ifc

AMENDMENT TRANSMITTAL LETTER				Docket No. SON-1285/DIV	
Application No. 10/752,705-Conf. #5551	Filing Date January 8, 2004	Examiner Jack S. J. Chen	Art Unit 2813		
Applicant(s): Takashi Noguchi et al.					
Invention: FIELD EFFECT TRANSISTOR AND FABRICATION THEREOF, SEMICONDUCTOR DEVICE AND FABRICATION THEREOF, LOGIC CIRCUIT INCLUDING THE SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR SUBSTRATE					
TO THE COMMISSIONER FOR PATENTS					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	13	- 27 =		x	0.00
Independent Claims	3	- 7 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ 0.00 A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. 18-0013 as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
Ronald P. Karanen Attorney Reg. No.: 24,104 RADER, FISHMAN & GRAUER PLLC 1233 20th Street, N.W. Suite 501 Washington, DC 20036 (202) 955-3750				Dated: November 13, 2006	



Docket No.: SON-1285/DIV
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takashi Noguchi, et al

Conf. # 5551

Application No.: 10/752,705

Group Art Unit: 2813

Filed: January 8, 2004

Examiner: Jack S.J. Chen

For: FIELD EFFECT TRANSISTOR AND
FABRICATION THEREOF,
SEMICONDUCTOR DEVICE AND
FABRICATION THEREOF...

RESPONSE TO NON-FINAL ACTION OF AUGUST 11, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In full and timely response to the Office Action mailed on August 11, 2006, please further amend the above-identified application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 15 of this paper.